

**Amendments to the Claims**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of claims:**

1. (Currently amended) An active matrix electroluminescent display device comprising an array of display pixels, each pixel comprising:
  - an electroluminescent (EL) display element;
  - a drive transistor for driving a current through the display element;
  - a current sampling resistor, wherein the EL display element, the drive transistor and the current sampling resistor are in series between first and second power lines; and
  - circuitry comprising at least one sampling transistor for providing a one or more feedback ~~signal or~~ signals representing the voltage drop across the current sampling resistor to at least one feedback line; and
    - an address transistor, connected between a data input line and the gate of the drive transistor,
    - the gates of the address transistor and the or each sampling transistor being controlled by a shared address line,wherein the display device further comprises processing means for processing pixel drive signals in dependence on the feedback signal or signals.
2. (Currently amended) A device as claimed in claim 1, wherein the circuitry for providing a feedback signal or signals comprises a first sampling transistor connected between one terminal of the current sampling resistor and a first said feedback line.
3. (Currently amended) A device as claimed in claim 2, wherein the circuitry for providing a feedback signal or signals further comprises a second said sampling transistor

connected between the other terminal of the current sampling resistor and a second feedback line.

4. (Deleted)

5. (Currently amended) A device as claimed in claim 1 [4], wherein each pixel further comprises: a second address transistor, wherein the second address transistor is connected between the one terminal of the current sampling resistor and a current drain line.

6. (Original) A device as claimed in claim 5, wherein the second address transistor is controlled by the shared address line.

7. (Currently amended) A device as claimed in claim ~~any one of claims~~ 1, wherein the processing means comprises:

a first amplifier which receives the feedback signal or signals and derives therefrom an output dependent on the current flowing through the current sampling resistor[.]; and

a second amplifier which receives the output dependent on the current flowing through the current sampling resistor and the pixel drive signal and provides a modified pixel drive signal.

8. (Currently amended) A device as claimed in claim 1, wherein the processing means comprises:

a first amplifier which receives the feedback signal or signals and derives therefrom an output dependent on the current flowing through the current sampling resistor[.];

a sample and hold circuit for holding the output value[.]; and

a second amplifier for receiving the held output value and the output dependent on the current flowing through the current sampling resistor.

9. (Currently amended) A device as claimed in claim 1, wherein each pixel further comprises:

an address transistor, connected between a data input line and the gate of the drive transistor, wherein the gates of the address transistor and the or each sampling transistor are controlled by a shared address line, and

wherein the processing means comprises:

a first amplifier which receives the feedback signal or signals and derives therefrom an output dependent on the current flowing through the current sampling resistor;

a sample and hold circuit for holding the output value; and

a second amplifier for receiving the held output value and the output dependent on the current flowing through the current sampling resistor, wherein the data input line is switchable between a power supply line voltage ( $V_{SUPPLY}$ ) and the output of the second amplifier.

10. (Currently amended) A device as claimed in claim 8 ~~or~~ 9, wherein the device is operable in two modes, the modes comprising:

a first mode in which a desired pixel drive current is driven through the current sampling resistor and the second address transistor to the current drain line, and the output dependent on the current flowing through the current sampling resistor is stored; and

a second mode in which a current is driven through the drive transistor and the EL display element and the output dependent on the current flowing through the current sampling resistor is provided to the second amplifier for comparison with the stored output value, the second amplifier providing the data input line voltage.

11. (Currently amended) A method of addressing an active matrix electroluminescent display device comprising an array of display pixels, in which each pixel comprises an electroluminescent (EL) display element, a drive transistor for driving a current through the display

element and a current sampling resistor in series with the EL display element and the drive transistor, the method comprising, for each pixel:

applying a drive signal to the pixel representing a desired current;

~~obtaining a feedback signal representing the current flowing through the display element by sampling a voltage on the terminals of the resistor (30) in series with the EL display element;~~

switching a shared data line to control sampling of a voltage on the terminals of the resistor in series with the EL display element to obtain a feedback signal representing the current flowing through the display element; and

using the drive signal and the feedback signal to generate a modified pixel drive signal such that the current flowing is equal to the desired current.

12. (Original) A method as claimed in claim 11, wherein using the drive signal and the feedback signal comprises differentially amplifying the signals.

13. (Currently amended) A method as claimed in claim 11 ~~or 12~~, wherein sampling a voltage on the terminals of the resistor in series with the EL display element comprises tapping the voltage from each terminal to a differential amplifier.

14. (Currently amended) A method as claimed in claim 11 ~~or 12~~, wherein sampling a voltage on the terminals of a resistor in series with the EL display element comprises tapping the voltage from one terminal, and the voltage on the other terminal comprising a known supply voltage.

15. (Currently amended) A method of addressing an active matrix electroluminescent display device comprising an array of display pixels, in which each pixel comprises an electroluminescent (EL) display element, a drive transistor for driving a current through the display

element and a current sampling resistor in series with the EL display element and the drive transistor, the method comprising, for each pixel:

driving a desired current through the current sampling resistor and not through the display element;

~~obtaining a feedback signal representing the corresponding voltage drop across the current sampling resistor;~~

switching a shared data line to control sampling of a voltage on one or more terminals of the current sampling resistor to obtain a feedback signal representing the voltage drop across the current sampling resistor;

storing the feedback signal; and

using the stored feedback signal as a feedback control signal for subsequently driving current through the display element by applying a voltage to the gate of the drive transistor, the feedback control signal being used to determine the gate voltage.

16. (Original) A method as claimed in claim 15, wherein using the stored feedback signal comprises applying the stored feedback signal and a second feedback signal during driving of the display element to a differential amplifier, and using the differential amplifier output to control the drive transistor.

17. (Currently amended) A method as claimed in claim 16, wherein the second feedback signal is obtained by sampling a voltage on a said terminal ~~the terminals~~ of the current sampling resistor.